

## RESUME

**Name** **:** CHANDAN KUMAR BERA

**Permanent Address** : C/O Dilip Kumar Bera

South Inda, Sipahidanga

P.O. – Kharagpur

Dist. – Paschim Medinipore

Pin. - 721301

State – West Bengal

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**Carrier Objective**

To build a carrier as a junior engineer which help me to explore myself fully and realize my potential and also willing to work in challenging and creative environment.

**Current Status**

Student of B.Tech (Applied Electronics & Instrumentation Engineering) final year with an aggregate score of 7.11 (DGPA) up to 4th year (8th semester) student of Bankura Unnayani Institute of Engineering under W.B.U.T. (West Bengal University of Technology)

**Project Experience in B.Tech**

1. I have done a project work from **GLOBSYN PRACTICE SCHOOL** entitled “**Electricity bill generation** **application**”in course **‘Creating GUI applications using JAVA 5 ’** as an **Vocational Training** of my B.Tech (3rd semester) in summer vacation .

2. I had designed a Line follower device during 5th Sem by only using basic electronics components (with out Microprocessor chip)

3. I have trained in **INDUSTRIAL AUTOMATION (PLC PROGRAMMING)** comprising of

**i) HARDWARE LOGIC CONTROL & RELAY LOGIC APPLICATION.**

**ii) PROGRAMMING LOGIC CONTROLLER**

**iii) PROGRAMMING PLC BY SIMATIC MANAGER**

from **C.T.T.C , Bhubaneswar (MSME).** I have basic knowledge of SCADA & WINCC( SIEMENCE). I have knowledge of pneumatic & electro pneumatic system.

4. I have actively taken part in Final Year Project on **REAL TIME TRAFFIC MONITERING SYSTEM** where I was the sole or main designer of the project idea.

1. I have trained in VLSI & Embedded system from Central Tool Room and Training Centre (CTTC), Bhubaneswar. I have also trained in VHDL & Verilog HDL coading through XILINS, MICROWIND & ALTERA softwares.I have done 8 bit ALU project under this institution.

**Academic Qualifications**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| **Course** | **Council / University** | **Year of passing** | **Total marks / Point** | | **Marks /Credit points Obtained** | | **Percentage / YGPA** |
| **Semester** | **Point** | **Credit Points** | **SGPA** |
| B.Tech (Applied Electronics & Instrumentation Engineering) | West Bengal University of Technology | 2011 | 1st | 67 | 197 | 6.57 | 6.61 |
| 2nd | 87 | 206 | 6.65 |
| 3rd | 67 | 197 | 6.35 | 6.55 |
| 4th | 71 | 209 | 6.74 |
| 5th | 67 | 200 | 7.14 | 7.17 |
| 6th | 77 | 216 | 7.2 |
| 7th | 63 | 196 | 7.54 | 7.75 |
| 8th | 56 | 199 | 7.96 |
| Higher Secondary | West Bengal Council of Higher Secondary Education | 2007 | 500 | | 310  (Best of five) | | 61.4% |
| Matric | West Bengal Board of Secondary Education | 2005 | 800 | | 526 | | 65.75% |

**Interested Area in Applied Electronics & Instrumentation Engineering**

**Subject:** IndustrialInstrumentation,Digital Electronics ,Microprocessor

**Language Known**

English : Read, Write & Speak

Bengali : Read, Write & Speak

Hindi : Read & Speak

**Personal Details**

Name : Chandan Kumar Bera

Date of Birth : 9th January, 1989.

Father’s Name: Mr. Dilip Kumar Bera

Nationality : Indian

Gender : Male

Marital Status : single

Interest : Travelling, Listening to music, Reading story books.